

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. TOKOROZUKI, et al.
Serial No.: NEW
Filed: May 10, 2006
For: MANUFACTURING METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

PRELIMINARY AMENDMENT

Mail Stop: NEW APPS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 10, 2006

Sir:

Concurrently with the filing of the above-identified application, please amend the above-identified application, prior to calculation of the filing fee, as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.